



EXAMINATION SECTION

ACADEMIC YEAR: 2025-26

Revised Time Table - M.Tech. I Year I Semester (R25) Supplementary End Semester Examinations July 2026

TIMINGS: FN: 10:00 A.M to 01:00 PM

Date/Day	Session	Computer Science and Engineering (CSE)	VLSI Design and Embedded Systems (VES)
02.07.2026 (Thursday)	FN	Advanced Data Structures and Algorithms- 25MBCSETC01	CMOS Digital IC Design- 25MBVESTC01
04.07.2026 (Saturday)	FN	Research Methodology and IPR-25MBCOMTC01	
06.07.2026 (Monday)	FN	Natural Language Processing- 25MBCSEDC01	FPGA Architectures and Applications- 25MBVESDC03
07.07.2026 (Tuesday)	FN	Big Data Analytics- 25MBCSEDC04	Low Power VLSI Design-25MBVESDC04
08.07.2026 (Wednesday)	FN	Modern Database Management Systems- 25MBCSETC02	Advanced Microcontrollers and Signal Processors- 25MBVESTC02

Dated: 16.06.2026


Controller of Examinations

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE - 517 325, A. P.

To be read in all the M. Tech-(CSE, VES) I Year II Semester Classrooms

Copy to The Website The File Notice Board HoD-CSE HoD-ECE Transport Incharge

Note: HoD is requested to circulate among the faculty members concerned